

SVT Slow Control: Power and Temperature

Power to SVT from Wiener MPOD power supplies

General requirements from slow control

- Control and monitor all power supplies channels
- Monitor temperature and status
- Provide hardware and software interlock signal to power supplies

EPICS control and monitoring requirements

- Power to FE- and flange boards and high voltage bias through MPOD crate
- Control of hybrid power through SVT DAQ EPICS bridge
- Monitor hybrid and FE board temperatures and status

Schedule

- Week 12/16: SLAC receives power supplies
- Week 1/15: pass basic tests at SLAC [Hansson, Uemura]
- Week 3/17: EPICS bridge for SVT tested [Herbst]
- Week 5/19: EPICS control power for DAQ tests at SLAC [Egyian, Hansson, Uemura]
- Week 7/14: interlock integration tests [Egyian]

EPICS variables

Hybrid	Type	Variables
AVDD	SVT CA server	Set: ON/OFF Read: V, I
V125	SVT CA server	Set: ON/OFF Read: V, I
DVDD	SVT CA server	Set: ON/OFF Read: V, I
Temperature	SVT CA server	Read: T

FE Boards	Type	Variables
AVDD+	MPOD control	Set: ON/OFF, V Read: V, I
AVDD-	MPOD control	Set: ON/OFF, V Read: V, I
DVDD+	MPOD control	Set: ON/OFF, V Read: V, I
Temperature	SVT CA server	Read: T

Flange Boards	Type	Variables
DVDD	MPOD control	Set: ON/OFF, V Read: V, I

Bias voltage	Type	Variables
HV	MPOD control	Set: ON/OFF, V Read: V, I