



DCN No.
LAT-XR-07131-01

LAT PROJECT DOCUMENT CHANGE NOTICE (DCN)

SHEET 1 OF 2

ORIGINATOR: Mike DeKlotz **PHONE:** 650-926-4752 **DATE:** 8/16/05

CHANGE TITLE: DCN for LAT Flight Software Test Procedures **ORG.:**

DOCUMENT NUMBER	TITLE	NEW REV.
LAT-TD-07126	LAT FSW Qualification Test Procedure: DCMODE_001: ACD Diagnostics & Calibration	01
LAT-TD-07127	LAT FSW Qualification Test Procedure: DCMODE_002: ACD Diagnostics & Calibration	01
LAT-TD-07128	LAT FSW Qualification Test Procedure: DCMODE_003: TKR Diagnostics & Calibration	01
LAT-TD-07129	LAT FSW Qualification Test Procedure: NBTLMV_001: Narrowband Telemetry Housekeeping & Low-rate Science Data Verificaiton	01
LAT-TD-07130	LAT FSW Qualification Test Procedure: NBTLMV_002: Diagnostic Telemetry Verification	01

CHANGE DESCRIPTION (FROM/TO):

LAT-TD-07132-01- LAT FSW Qualification Test Procedure: FSWINI_001: FSW Initialization-SIU Primary Boot
 LAT-TD-07133-01- LAT FSW Qualification Test Procedure: FSWINI_002: FSW & LAT Initialization- Boot Self-Test & Boot Housekeeping Telemetry
 LAT-TD-07134-01- LAT FSW Qualification Test Procedure: FSWINI_003: FSW & LAT Initialization- Multiple Boot Images
 LAT-TD-07135-01- LAT FSW Qualification Test Procedure: FSWINI_004: FSW & LAT Initialization- SIU Hardware Reboot in response to the Signal on the Discrete Lines
 LAT-TD-07136-01- LAT FSW Qualification Test Procedure: FSWINI_005: FSW Initialization- EPU Primary Boot
 LAT-TD-07137-01- LAT FSW Qualification Test Procedure: FSWINI_006: FSW & LAT initialization- Reset Source
 LAT-TD-07138-01- LAT FSW Qualification Test Procedure: FSWINI_007: FSW & LAT Initialization- Storage & Retrieval of System Errors During SIU Primary Boot
 LAT-TD-07139-01- LAT FSW Qualification Test Procedure: FSWINI_008: FSW & LAT Initialization- Storage & Retrieval of System Errors During EPU Primary Boot
 LAT-TD-07140-01- LAT FSW Qualification Test Procedure: FSWINI_009: FSW & LAT Initialization- SIU Boot Status on Discrete Lines & SISU Boot Housekeeping Telemetry
 LAT-TD-07141-01- LAT FSW Qualification Test Procedure: FSWINI_010: FSW & LAT Initialization- SIU & EPU Secondary Boot
 LAT-TD-07142-01- LAT FSW Qualification Test Procedure: FSWINI_011: FSW & LAT Initialization- SIU & EPU Secondary Boot Error Mitigation
 LAT-TD-07143-01- LAT FSW Qualification Test Procedure: FSWINI_012: FSW & LAT Initialization- LAT SEU Protection
 LAT-TD-07144-01- LAT FSW Qualification Test Procedure: FSWINI_013: FSW & LAT Initialization- LAT Memory Scrubbing
 LAT-TD-07145-01- LAT FSW Qualification Test Procedure: FSWINI_014: FSW & LAT Initialization- Watchdog Management During Boot
 LAT-TD-07146-01- LAT FSW Qualification Test Procedure: FSWINI_015: FSW & LAT Initialization- Soft Reset
 LAT-TD-07152-01 - LAT FSW Qualification Test Procedure:FECALB_001: Charge Injection Calibration – TOT Measurements
 LAT-TD-07153-01- LAT FSW Qualification Test Procedure:FECALB_002: Charge Injection Calibration – TKR Threshold and Charge Scans
 LAT-TD-07154-01- LAT FSW Qualification Test Procedure: FECALB_003: Charge Injection Calibration – TKR Trigger Check
 LAT-TD-07155-01- LAT FSW Qualification Test Procedure: FECALB_004: Charge Injection Calibration – ACD Charge Injection
 LAT-TD-07156-01- LAT FSW Qualification Test Procedure: FECALB_005: Charge Injection Calibration – CAL Charge Injection
 LAT-TD-07157-01- LAT FSW Qualification Test Procedure: WBTLMV_001: Wideband Telemetry Verification – Science Data Format and Volume

REASON FOR CHANGE:

ACTION TAKEN: Change(s) included in new release DCN attached to document(s), changes to be included in next revision
 Other (specify):

DISPOSITION OF HARDWARE (IDENTIFY SERIAL NUMBERS):

DCN DISTRIBUTION:

- No hardware affected (record change only)
- List S/Ns which comply already:
- List S/Ns to be reworked or scrapped:
- List S/Ns to be built with this change:
- List S/Ns to be retested per this change:
-

SAFETY, COST, SCHEDULE, REQUIREMENTS IMPACT? YES NO

If yes, CCB approval is required. Enter change request number:

APPROVALS	DATE	OTHER APPROVALS (specify):	DATE
ORIGINATOR: M. DeKlotz (signature on file)	8/16/05		
ORG. MANAGER: D. Horn (signature on file)	8/16/05		
PSA- K. Burlingham (signature on file)	8/16/05		
DCC RELEASE: Natalie Cramar (signature on file)	8/16/05	Doc. Control Level: <input checked="" type="checkbox"/> Subsystem <input type="checkbox"/> LAT IPO <input type="checkbox"/> GLAST Project	

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Continuation:

LAT-TD-07158-01- LAT FSW Qualification Test Procedure: FILMGT_001: File Management Verification
LAT-TD-07159-01- LAT FSW Qualification Test Procedure: MEMMGT_002: Memory Load Data
LAT-TD-07160-01- LAT FSW Qualification Test Procedure: NBTLMV_003: ACD HSK Anomaly Response and Alert Telemetry Verification
LAT-TD-07161-01- LAT FSW Qualification Test Procedure: OPMODE_001: LAT Operational Mode Control
LAT-TD-07162-01- LAT FSW Qualification Test Procedure: THRMCS_001: LAT Thermal Control System
LAT-TD-07163-01- LAT FSW Qualification Test Procedure: VSGIFV_001: Discrete Signal Interfaces
LAT-TD-07164-01- LAT FSW Qualification Test Procedure: CMDFNC_003: 1553 Interface and Command Functional Verification
LAT-TD-07054-01- LAT FSW Qualification Test Procedure: EVTPMO_001: Event Performance Monitoring and Verification – Software-Related Deadtime
LAT-TD-07055-01- LAT FSW Qualification Test Procedure: EVTPMO_002: Event Performance Monitoring and Verification –Acquisition of VETO Rates from the GEM
LAT-TD-07056-01- LAT FSW Qualification Test Procedure: EVTPMO_003: Event Performance Monitoring and Verification – Level 1 Trigger Rates
LAT-TD-07057-01- LAT FSW Qualification Test Procedure: EVTPMO_004: Event Performance Monitoring and Verification – Monitor CNO Rates
LAT-TD-07112-01- LAT FSW Qualification Test Procedure: EVTFIL_001: Event Filtering – Interface from the Event Builder
LAT-TD-07113-01- LAT FSW Qualification Test Procedure: EVTFIL_002: Event Filtering – Event Filter Rates and Capacity
LAT-TD-07114-01- LAT FSW Qualification Test Procedure: EVTFIL_003: Event Filtering – Event Filter Reprogramming
LAT-TD-07115-01- LAT FSW Qualification Test Procedure: EVTFIL_004: Event Filtering – Event Filter Bypass

	Document # LAT-TD-07142-01	Date effective 8/16/05
	Author(s) Igor Kouzmenko	Supersedes
	Subsystem/Office Electronics & DAQ Subsystem	
Document Title LAT FSW Qualification Test Procedure: FSWINI_011: FSW and LAT Initialization – SIU and EPU Secondary Boot Error Mitigation		

CHANGE HISTORY LOG

Revision	Effective Date	Description of Changes
01	8/16/05	Original

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1. **SCOPE**

This document describes the procedure followed for qualification testing of the LAT FSW. This Qualification Test Procedure document describes one of the qualification tests executed to verify compliance with the requirements defined in the “Flight Software Specification – Level III” (LAT-SS-00399).

1.1 Test Suite

FSWINI

The Test Suite is a logical grouping of a set of tests based on common functionalities and can be executed as a suite of tests in some order when the pre-conditions and post-conditions for each test within the suite have been met successfully.

This test procedure *FSWINI_011* belongs in the Test Suite *FSWINI* under the *FST* Project.

1.2 Test ID

FSWINI_011

Test *FSWINI_011* contains one main script, *FSWINI_011*.

Telecommands

APID	FC	Cmd. Packet (L)	Description (L)
<u>0x644</u>	0	LMEMDUMPMEM	Memory Data Dump
0x662	1	<u>LIMLOADSHED</u>	Load Shed
0x663		<u>LIMMAINFEEDON</u>	Enable Main Power Feed
0x663	7	<u>LIMPOWEROFF</u>	Disable Instrument Power
0x663	6	<u>LIMPOWERON</u>	Enable Instrument Power
<u>0x644</u>		LMEMLOADREG	Processor Register Write
<u>0x640</u>	1	LBRESET	Warm reboot

Telemetry

APID	Tlm. Packet (L)	Description (L)
<u>0x200</u>	LBTHKP	Boot housekeeping telemetry
<u>0x261</u>	LBTEPU0HKP	EPU 0 Boot Housekeeping Telemetry
<u>0x262</u>	LBTEPU1HKP	EPU 1 Boot Housekeeping Telemetry

1.3 Requirement(s) Tested

The Qualification Test Procedure described herein is performed to verify that the FSW satisfies the following requirement(s), quoted from the Flight Software Specification – Level III:

Requirement Number	Requirement Name	Requirement	Level of Requirements Verification in This Test
5.3.1.10.5	Secondary Boot Checksum	The FSW shall calculate a checksum over the secondary boot image.	Full
5.3.1.10.6	Secondary Boot Errors	The FSW shall record errors from the secondary boot process in a global memory area suitable for later examination.	Full

If the requirement(s) quoted above cite external documents (e.g., “...Further details are provided in [11]”), consult LAT-SS-00399 for the list of citations.

2. DEFINITIONS AND ACRONYMS

The following terms, abbreviations, and acronyms are used in this document:

2.1 Definitions

Hz	Hertz, unit of frequency
s, sec	seconds
V	Volt
W	Watt
s, sec	seconds

2.2 Acronyms

CAL	Calorimeter
EGSE	Electrical Ground Support Equipment
GASU	Global trigger Anti-collision Spacecraft Unit
PBC	Primary Boot Code (FSW package)
PTR	Post Test Review
SBC	Secondary Boot Code (FSW package)
TEM	Tower Electronics Module
TKR	Tracker
TPS	Tower Power Supply
TRR	Test Readiness Review
QAE	Quality Assurance Engineer
TE	Test Engineer

3. REFERENCES

The list below provides documents that are to be used as references for this procedure:

3.1 **Applicable Documents**

<u>Document Number</u>	<u>Description</u>
<u>SPECIFICATIONS</u>	
LAT-SS-00399	LAT Flight Software Level III Specification
1196 EI-S46310-000	GLAST 1553 Bus Protocol Interface Control
LAT-TD-02659	LAT Flight Software Telecommand and Telemetry Formats
LAT-TD-0561	The Virtual Spacecraft (VSC)
<u>PROCEDURES</u>	
N/A	N/A
<u>PLANS</u>	
LAT-MD-00039	Performance Assurance Implementation Plan
LAT-MD-00078	GLAST LAT System Safety Program Plan
LAT-MD-00104	LAT FSW Management Plan
LAT-MD-00404	LAT Contamination Control Plan
LAT-MD-00408	LAT Program Instrument Performance Verification Plan
LAT-SS-00296	T & DF Test Plan
LAT-TD-00297	LAT Electronics Test Plan
LAT-TD-00786	LAT Flight Software Test Plan
<u>OTHER</u>	
LAT-MD-00091	GLAST Quality Manual
LAT-MD-00471	Control of Nonconforming Product
LAT-MD-00472	Corrective and Preventative Action

4. REQUIREMENTS

This section lists the requirements that shall be followed during the LAT FSW Qualification Testing process.

The Performance Assurance Implementation Plan, LAT-MD-00039, shall be utilized to ensure that the products produced by the GLAST LAT project intended for design qualification, flight and critical ground support equipment usage meet the required levels of quality and functionality for their intended purposes.

The LAT Program Instrument Performance Verification Plan, LAT-MD-00408, shall be utilized to address the testing to be performed at the unit/subsystem and instrument level for flight qualification, proto-flight and acceptance testing phases. Also included are the necessary processes/procedures and systems assurance activities.

4.1 Test Data, Equipment and Software

This procedure shall follow the requirements found in the Control of Nonconforming Product, LAT-MD-00471. This document establishes the method to identify and control nonconforming product developed by the LAT project team.

4.2 Quality Assurance

This procedure shall follow the requirements found in the Corrective and Preventative Action document, LAT-MD-00472 and the GLAST Quality Manual, LAT-MD-00091.

The Corrective and Preventative Action document establishes the method to be used to initiate, implement, evaluate and record corrective and preventive actions. The GLAST Quality Manual defines the methods implemented by the GLAST LAT project to ensure consistent quality of all processes for procurement, design, development and production of flight hardware, flight software and all associated ground support equipment interfacing with flight hardware and software.

4.3 Safety

This procedure shall follow the requirements found in the GLAST LAT System Safety Program Plan, LAT-MD-00078. This document defines all phases of the LAT program including: design, development, fabrication, handling, transportation, storage, test, assembly and operation.

WARNING: When high voltages are present extreme care should be exercised.

4.4 Warnings, Cautions, and Notes

The following SAFETY ALERTS are intended to create awareness of the potential safety hazards and the steps that must be taken to avoid accidents. These same alerts are used throughout this document to identify specific hazards that may endanger personnel and/or equipment.

Identification of every conceivable hazardous situation is impossible. Therefore, all personnel have the responsibility to diligently exercise safe practices whenever exposed to this equipment.

WARNING: Indicates a potential hazardous situation which, if not avoided, could result in death or injury.

CAUTION: Indicates a potential hazardous situation which, if not avoided, could result in damage to equipment.

Note: Indicates a notification of information that is important, but not hazard related.

4.5 General Instructions

This qualification test procedure shall be conducted on a formal basis to its latest approved and released version. The designated Software QAE shall be notified 24 hours prior to the start of this procedure. Software QAE may monitor the execution of all or part of this procedure should they elect to do so.

The Test Engineer conducting this test shall read this document in its entirety and resolve any apparent ambiguities before beginning the procedures described herein.

Deviations from the procedures described in this document and breaks in hardware or software configuration can only be initiated by the Test Engineer, must be approved by QA, and must be documented in Appendix A.

Any nonconformance/defect/anomaly is to be reported in JIRA. Refer to the LAT Flight Software Test Plan LAT-TD-00786 for guidance. Do not alter or break configuration if a failure occurs. Notify Software Quality Assurance.

All success conditions for a test must be met for the test to pass.

5. SETUP

This section describes the hardware and software configuration used for the qualification test described later in this document. Any break from configuration or deviation from a particular procedure must be authorized by the Quality Assurance Engineer and documented in Appendix A.

5.1 Hardware Setup

The list below indicates the equipment that is used to execute the tests described in this document.

Hardware Unit	Manufacturer	GLAT Number (and Hardware Sub-Units by GLAT Number)	Firmware Version (where applicable)
Virtual Spacecraft (VSC)	SLAC		
Spacecraft Interface Unit (SIU)	SLAC		
Event Processing Unit (EPU): 2 (EPU0 and EPU1)	SLAC		
Global Trigger AEM Signal Distribution Unit (GASU)	SLAC		
Power Distribution Unit (PDU)	SLAC		
1553 cables and couplers	SLAC		
Unix or Linux Host Establishes connection between VSC and the terminal from which the test is run.			
Power supply for SIU	SLAC		

The Figure below depicts the Testbed on which this qualification test is performed. The particular hardware units utilized in this test are itemized by GLAT number and firmware version in the preceding table.

Software	Description of Software	Software Version Number (or Specify Attachment Number)	Path to Attachment (If Applicable)
LTX	LAT Test Executive		
FMX	File Management Extra		
VSC	Virtual Spacecraft system software		
VPI	VSC Python/Proxy Interface		

5.2.2 Test Scripts

The following table identifies the test scripts that are run to execute this qualification test. The “Script Version Number” column identifies which version of the script is being used; alternatively, this column identifies the hardcopy attachment to this document that records the version of the script being used (e.g., “Attachment 1”). The “Path to Attachment” column identifies the directory in which the electronic copy of any hardcopy attachment is saved (if applicable).

Test Script	Description of Test Script	Script Version Number (or Specify Attachment Number)	Path to Attachment (If Applicable)
FSWINI_011.py	Main LTX qualification test execution script.		
FSWINI_011.xml	LTX test description file for test FSWINI_011.		

5.2.3 Flight Software

This qualification test is performed on a complete, integrated Candidate Release of FSW. All FSW libraries under test are final Flight Unit Candidate versions. The test described in this document is designed to evaluate the particular FSW packages and constituents listed in the following table; for clarity, only those constituents that are the focus of the test are listed below.

FSW Package	Constituent(s)
PBC	
SBC	
LIM	

The Candidate Release into which these FSW constituents are integrated is identified in the following table. The “Candidate Release Build Number” column specifies the build number of the FSW Candidate Release under test (e.g., “B0-0-1”). The “Candidate Release Attachment Number” column identifies which hardcopy attachment to this document confirms the name and version number of the full set of FSW packages and constituents used in the test (e.g., “Attachment 2”). The “Path to Attachment” column specifies where in the test repository an electronic copy of the hardcopy attachment has been saved.

Candidate Release Build Number	Candidate Release Attachment Number	Path to Attachment

5.3 Setup Validation

5.3.1 Hardware Validation

The following signatures confirm that the Test Engineer and Quality Assurance Engineer have verified the GLAT numbers, firmware version numbers, and the proper connection of all hardware listed in the table in Section 5.1.

Date Time Test Engineer QAE

5.3.2 Software Validation

The Test Engineer performs the following procedure to validate the software setup for this qualification test and records completion of the setup validation steps in the space provided.

Step No.	Description of Step	Step Outcome
1	Obtain version numbers of the test tools used to perform this qualification test by running the xxxx script. Pick up and label the hardcopy report generated by the xxxx script and attach it to this test procedure document. Record (1) the label for the attachment in table and (2) the directory location to which an electronic copy of the report was saved in Section 5.2.1.	Complete/ Not Complete
2	Obtain version numbers of the tesc scripts used to perform this qualification test by running the xxxx script. Pick up and label the hardcopy report generated by the xxxx script and attach it to this test procedure document. Record (1) the label for the attachment and (2) the directory location to which an electronic copy of the report was saved in Section 5.2.2.	Complete/ Not Complete
3	Obtain the versions of the FSW packages loaded on the targets by running the <i>version_test</i> script in the Boot mode . Pick up and label the hardcopy report generated by the <i>version_test</i> script and attach it to this test procedure document. Record (1) the label for the attachment and (2) the directory location to which an electronic copy of the report was saved in Section 5.2.3.	Complete/ Not Complete

The following signatures confirm that, using the procedure described in the previous table, the Test Engineer and Quality Assurance Engineer have verified that all versions of test support software, test scripts, and FSW constituents match those identified in Section 5.2.

Date

Time

Test Engineer

QAE

6. TEST PROCEDURE FOR FSWINI 011

6.1 Test Objective

This test verifies the ability of FSW to calculate a checksum over the secondary boot image and record errors from the secondary boot process in a global memory area suitable for later examination.

This Test Objective is broken down into the following Test Sub-Objectives.

Number	Test Sub-Objective
1	Verify the checksum calculation over the SIU Secondary Boot image.
2	Verify that SIU processor record errors from the secondary boot process in a global memory area.
3	Verify the checksum calculation over the EPU0 Secondary Boot image.
4	Verify that EPU0 processor record errors from the secondary boot process in a global memory area.
5	Verify the checksum calculation over the EPU1 Secondary Boot image.
6	Verify that EPU1 processor record errors from the secondary boot process in a global memory area.

6.2 Test Input Files

The following table identifies all auxiliary files (e.g., Front End Simulator data files, GLEAM data files) used as inputs to this qualification test. Note that not all qualification tests use input data of this type. The “Input File Version Number” column identifies the version number of the auxiliary file being used; alternatively, this column identifies the hardcopy attachment to this document that records the version of the file being used (e.g., “Attachment 1”). The “Path to Attachment” column specifies where in the test repository an electronic copy of the hardcopy attachment has been saved .

Input File	Description of Input File	Input File Version Number (or Specify Attachment Number)	Path to Attachment (If Applicable)
N/A	N/A	N/A	N/A

6.3 Test Preparation

After the hardware and software setup has been validated, steps may be required to place the hardware and FSW in an operational mode in which the qualification test can be performed or otherwise complete preparations for the test to begin.

The Test Engineer carries out the following procedure to prepare for qualification testing and records completion of the test preparation steps in the space provided.

Step No.	Description of Step	Step Outcome
1	Power up the Virtual Spacecraft and SIU.	Complete/ Not Complete

Step No.	Description of Step	Step Outcome
2	Start CMX by issuing the following command from the host machine: \$ cmx start	Complete/ Not Complete
3	On the host machine, change directory to the location of the test scripts by issuing the following command: \$ cd /afs/slac.stanford.edu/g/glast/flight/FST/source/FSWINI/Vx-x-x/FSWINI_011/	Complete/ Not Complete

The Test Engineer and Quality Assurance Engineer verify that all test preparation steps are complete.

_____ Date _____ Time _____ Test Engineer _____ QAE

6.4 Test Procedure

After the test setup has been validated, and test preparation steps are complete, the Test Engineer proceeds with the qualification test procedure itself, as described below, and records the outcome of each step during test execution.

A summary of the steps involved in this test procedure is provided below. It is primarily intended as a description of how the Test Objectives are verified in this test and how data is analyzed to qualify if each of the test objectives have been met. The outcome of each step is either “Complete” or “Not Complete” (for steps which involve no analysis or verification); or, “Pass” or “Fail” (for steps involved in verifying completion of test objectives and sub-objectives).

Step No.	Description of Step	Step Outcome
1	Run the FSWINI_011 test script by typing the following command at the shell: \$ ltx run <FSWINI_011	Complete/ Not Complete
2	LTX automatically sends several output files to the nfs file space. Print the /nfs/slac/g/glast/ltx000/ltx_repos/FST/FSWINI/Vx-x-x/FSWINI_0xx/mddyy_hhmmss log file, retrieve it from the printer, label it, and attach it to this test procedure. Record the label written on the printed log file: _____ Record the full nfs path and filename of the soft copy of this output file: _____ _____	Complete/ Not Complete

Step No.	Description of Step	Step Outcome
3	<p>Review the xxx output file and verify that the following entry appears:</p> <p style="text-align: center;">FSWINI_011:(1) Pass</p> <p>The test verifies that the parameters of boot housekeeping telemetry (Hi Scrub address, boot type, boot mode, etc.) by the reception of SIU boot housekeeping telemetry, LBTHKP (APID 0x200) are correct.</p> <p>Verifies that the Secondary Boot Command LBTRTOSEXEC issued to the SIU from the SC performs the secondary boot process to completion from the RAM or EEPROM (lower or upper banks).</p> <p>Verify that checksum calculation over the RTOS image is handled in the PBC package.</p> <p>Verifies that there is no explicit acknowledgement; thus, image checksums are correct.</p> <p>Send MEMLOADREG command to the Register address 0x9c with a value of 0x00000400 to corrupt image.</p> <p>Execute LMEMDUMPMEM command to verify that image is corrupted.</p> <p>Restart the SIU by sending external reboot command LPBCRESET.</p> <p>Verify that the checksum over the SBC modules 0 and 1 is handled in the SBC package.</p> <p>Send Secondary Boot Command LBTRTOSEXEC again to execute the bad RTOS image.</p> <p>The PBC FSW will send back an explicit error. Boot housekeeping telemetry will return an exception type of reboot occurred in the LPCBOOTYPE field.</p> <p>Error fields in this telemetry are initialized to fresh values.</p> <p>This step completes sub-objective 1.</p>	<p>Pass/ Fail</p>
4	<p>Review the xxx output file and verify that the following entry appears:</p> <p style="text-align: center;">FSWINI_011:(2) Pass</p> <p>Retrieve Memory Dump from global CPU memory to ensure that boot errors are recorded. The test script issues the LMEMDUMPMEM telecommand.</p> <p>Confirm that the current dump command was received and processed (by checking the LPBCBOOTYPE, LPBCTOTALERRCNT, LPBCNEXTERRWORD, LPBCTRECVCNT, LPBCTACCCNT, and LPBCLASTERRWORD fields).</p> <p>This step completes sub-objective 2.</p>	<p>Pass/ Fail</p>
5	<p>Then the test script issues an LLIMLOADSHED command (all of LIM related commands will be tested in OPMODE).</p> <p>Then VSC issues the LLIMMAINFEEDON telecommand to power up the PDU and GASU and the LLIMPOWERON command to power up EPU0 and EPU1. In response to a power up, EPU0 and EPU1 should begin a COLD primary boot.</p> <p>If the EPUs FSW cannot execute these operations, LTX prints an error message to the screen and exits, aborting the test.</p> <p>If the test is not aborted, mark 'Complete' for the Step Outcome.</p>	<p>Complete/ Not Complete</p>

Step No.	Description of Step	Step Outcome
6	<p>Review the xxx output file and verify that the following entry appears:</p> <p>FSWINI_011:(3) Pass</p> <p>The test verifies that the parameters of boot housekeeping telemetry (Hi Scrub address, boot type, boot mode, etc.) by the reception of EPU0 boot housekeeping telemetry, LBTEPU0HKP (APID 0x261) are correct.</p> <p>Verifies that the Secondary Boot Command LBTRTOSEXEC issued to theEPU0 from the SC performs the secondary boot process to completion from the RAM or EEPROM (lower or upper banks).</p> <p>Verify that checksum calculation over the RTOS image is handled in the PBC package.</p> <p>Verifies that there is no explicit acknowledgement; thus, image checksums are correct.</p> <p>Send MEMLOADREG command to the Register address 0x9c with a value of 0x00000400 to corrupt image.</p> <p>Execute LMEMDUMPMEM command to verify that image is corrupted.</p> <p>Restart the EPU0 by sending external reboot command LPBCRESET.</p> <p>Verify that the checksum over the SBC modules 0 and 1 is handled in the SBC package.</p> <p>Send Secondary Boot Command LBTRTOSEXEC again to execute the bad RTOS image.</p> <p>The PBC FSW will send back an explicit error. Boot housekeeping telemetry will return an exception type of reboot occurred in the LPCBOOTYPE field.</p> <p>Error fields in this telemetry are initialized to fresh values.</p> <p>This step completes sub-objective 3.</p>	<p>Pass/ Fail</p>
	<p>Review the xxx output file and verify that the following entry appears:</p> <p>FSWINI_011:(4) Pass</p> <p>Retrieve Memory Dump from global EPU0 memory to ensure that boot errors are recorded. The test script issues the LMEMDUMPMEM telecommand.</p> <p>Confirm that the current dump command was received and processed (by checking the LPBCBOOTYPE, LPBCTOTALERRCNT, LPBCNEXTERRWORD, LPBCTCREVCNT, LPBCTCACCCNT, and LPBCLASTERRWORD fields).</p> <p>This step completes sub-objective 4.</p>	<p>Pass/ Fail</p>

Step No.	Description of Step	Step Outcome
	<p>Review the xxx output file and verify that the following entry appears:</p> <p>FSWINI_011:(5) Pass</p> <p>The test verifies that the parameters of boot housekeeping telemetry (Hi Scrub address, boot type, boot mode, etc.) by the reception of EPU1 boot housekeeping telemetry, LBTEPU1HKP (APID 0x262) are correct.</p> <p>Verify that the Secondary Boot Command LBTRTOSEXEC issued to theEPU1 from the SC performs the secondary boot process to completion from the RAM or EEPROM (lower or upper banks).</p> <p>Verify that checksum calculation over the RTOS image is handled in the PBC package. ?</p> <p>Verifies that there is no explicit acknowledgement; thus, image checksums are correct.</p> <p>Send MEMLOADREG command to the Register address 0x9c with a value of 0x00000400 to corrupt image.</p> <p>Execute LMEMDUMPMEM command to verify that image is corrupted.</p> <p>Restart the EPU1 by sending external reboot command LPBCRESET.</p> <p>Verify that the checksum over the SBC modules 0 and 1 is handled in the SBC package.</p> <p>Send Secondary Boot Command LBTRTOSEXEC again to execute the bad RTOS image.</p> <p>The PBC FSW will send back an explicit error. Boot housekeeping telemetry will return an exception type of reboot occurred in the LPCBOOTYPE field.</p> <p>Error fields in this telemetry are initialized to fresh values.</p> <p>This step completes sub-objective 5.</p>	<p>Pass/ Fail</p>
	<p>Review the xxx output file and verify that the following entry appears:</p> <p>FSWINI_011:(6) Pass</p> <p>Retrieve Memory Dump from global EPU1 memory to ensure that boot errors are recorded. The test script issues the LMEMDUMPMEM telecommand.</p> <p>Confirm that the current dump command was received and processed (by checking the LPBCBOOTYPE, LPBCTOTALERRCNT, LPBCNEXTERRWORD, LPBCTCREVCNT, LPBCTCACCCNT, and LPBCLASTERRWORD fields).</p> <p>This step completes sub-objective 6.</p>	<p>Pass/ Fail</p>

Initial to confirm.

Date Time Test Engineer QAE

7. TEST POST-CONDITIONS AND OVERALL OUTCOME

7.1 Test Post-Conditions

The expected state of the FSW and/or Testbed at the conclusion of this qualification test is indicated in the following table.

No.	Post-Condition	Post-Condition Met? (Yes/No)
1	Secondary boot is complete on the SIU and EPU. These CPUs are operating in Application Mode.	

The Test Engineer and Quality Assurance Engineer verify that all test post-conditions are met.

 Date Time Test Engineer QAE

7.2 Overall Outcome of FSWINI_011

Based on the analysis of the test results, the overall outcome of Test FSWINI_011 is as follows:

- Passed** - all of the expected outcomes for the test were confirmed
- Failed** - one or more of the test outcomes were not confirmed

 Date Test Engineer QAE

8. CERTIFICATION

I certify that the information obtained under this test procedure is as represented and the information recorded in this document is complete and correct. Any deviations from test procedures described herein are identified in Appendix A.

Date Test Engineer (Print Name) Test Engineer (Signature)

I certify that the information obtained through execution of this test procedure is as represented and the information recorded in this document is complete and correct. Execution of the test, storage of the results, and verification of outcomes were carried out in accordance with quality standards defined in the GLAST Quality Manual (LAT-MD-00091).

Date Software QA Engineer (Print Name) Software QA Engineer (Sign)

I certify that the information obtained under this test procedure is as represented and the information recorded in this document is complete and correct. The test procedure, as designed and executed, does indeed verify that the FSW functionality under test satisfies the corresponding requirements from the Flight Software Specification – Level III.

Date FSW Manager (Print Name) FSW Manager (Signature)

APPENDIX A: DEVIATIONS FROM THE QUALIFICATION TEST PROCEDURE

This section details any deviations from the hardware configuration, software configuration, or test procedure followed during the execution of the test or tests described in this Qualification Test Procedure document. All deviations from the approved procedure are agreed to by the Test Engineer and the Software Quality Engineer during the test execution session. All deviations must be reported during the Post Qualification Test Review, where their impact on the test results will be evaluated.

Hardware Deviations

Describe any deviations from the hardware configuration defined in Section 5.1. Name the hardware that was modified and describe the modifications. If hardware is *replaced* during execution of the test, name the replaced hardware, the manufacturer, and list an identification number (e.g., GLAT ID number).

Software Deviations

Describe any changes made to the software configuration under test or the software configuration used to support test execution, as defined in Section 5.2. Give version numbers of all FSW packages and test packages that were modified. Describe how the contents of the modified software load were verified. Describe these deviations for each test that was modified.

Procedural Deviations

Specify any deviations from the test procedure for the test being executed. If this document contains more than one test procedure, list the procedure by number (e.g., “MEMMGT_001”). List by number the steps modified or skipped. Provide a numbered sequence listing any added steps. Describe these deviations for each test that was modified.
