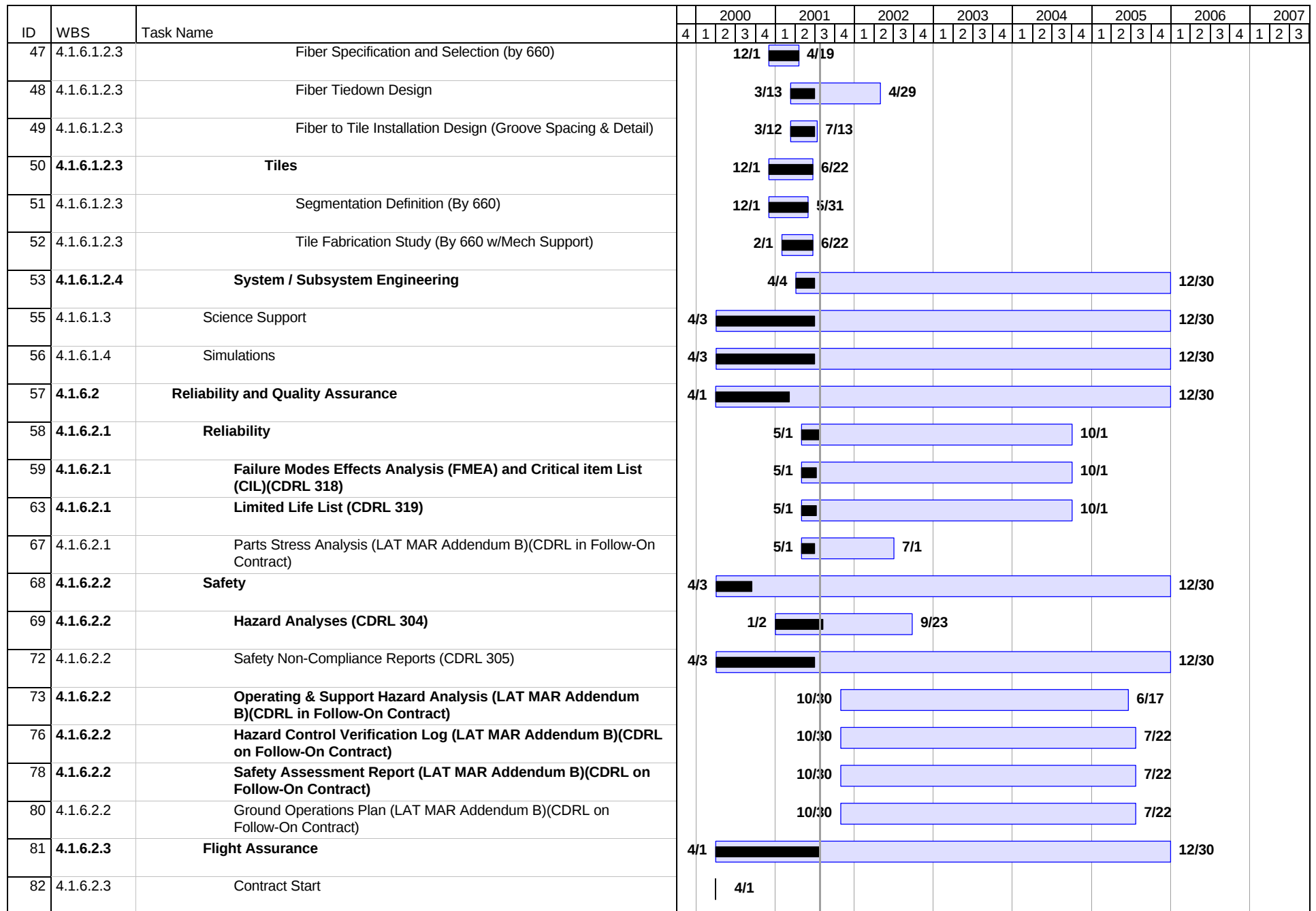


Anti-Coincidence Detector Subsystem Schedule (Preliminary)

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Anti-Coincidence Detector Subsystem Schedule (Preliminary)



Anti-Coincidence Detector Subsystem Schedule (Preliminary)

ID	WBS	Task Name		2000				2001				2002				2003				2004				2005				2006				2007		
			4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3				
83	4.1.6.2.3	Launch + 4 months					4/1																											
84	4.1.6.2.3	Quality Plan (CDRL 301)					2/1			7/27																								
89	4.1.6.2.3	Quality Manual (CDRL 320)	4/3																															
91	4.1.6.2.3	Non-Conformance Reports (NCRs) (CDRL 321)	4/3																															
93	4.1.6.2.3	Analyse Requirements Documents (CDRL 302)	4/3																															
94	4.1.6.2.3	Action Items/Peer review Minutes (CDRL 306)	4/3																															
95	4.1.6.2.3	Information to Prepare PRA, FTA, & Risk Assessments (CDRL 324,325,326)	4/3																															
96	4.1.6.2.4	Parts Control	4/3																															
97	4.1.6.2.4	Parts Identification List (PIL)(CDRL 310)					5/21																											
101	4.1.6.2.4	Alert/Advisory Responses (CDRL 311)	4/3																															
102	4.1.6.2.5	Materials	4/3																															
103	4.1.6.2.5	Materials, Process & Lubrication Lists (CDRL 314, 315, 316)					7/26																											
106	4.1.6.2.5	Material Usage Agreements (CDRL 313)	4/3																															
107	4.1.6.2.5	Materials Processes (CDRL 317)	4/3																															
108	4.1.6.2.5	PWB Coupons Test Reports (LAT MAR Addendum B)(CDRL on Follow-On Contract)	4/3																															
110	4.1.6.3	Tile Shell Assembly (TSA)																																
111	4.1.6.3.1	Tile Shell Assembly (TSA) Analysis / Design					3/21																											
112	4.1.6.3.1	ACD SRR					3/21			3/22																								
113	4.1.6.3.1	TSA Integration & Test and Handling Procedures																																
114	4.1.6.3.1	TDA Installation on TDA Tiedown																																
115	4.1.6.3.1	Installation Drawing & Procedure																																
116	4.1.6.3.2	Tile Detector Assemblies (TDAs)					1/22																											
117	4.1.6.3.2.1	Tile Detector Assembly (TDA) Analysis / Design					1/22																											

Anti-Coincidence Detector Subsystem Schedule (Preliminary)

ID	WBS	Task Name
118	4.1.6.3.2.1	Title Detector Assys (TDAs)
119	4.1.6.3.2.1	TDA Assembly Procedure
120	4.1.6.3.2.1	Prototype TDA Assembly
121	4.1.6.3.2.1	Prototype TDA Acceptance Testing
122	4.1.6.3.2.1	Performance Testing by 660
123	4.1.6.3.2.1	Thermal Vacuum Cycling on at Least One Unit
124	4.1.6.3.2.1	PMT/Fiber Connector - Prototype
125	4.1.6.3.2.1	Design & Procedure
126	4.1.6.3.2.1	Fabrication of Prototype Connector
127	4.1.6.3.2.1	Acceptance Testing - Light Yield, repeatability, thermal
128	4.1.6.3.2.1	WSF/Clear Fiber Connector - Prototype
129	4.1.6.3.2.1	Design & Procedure
130	4.1.6.3.2.1	Fabrication of Prototype Connector
131	4.1.6.3.2.1	Acceptance Testing - Light Yield, repeatability, thermal
132	4.1.6.3.2.1	Tile Layout & Fiber Routing Design
133	4.1.6.3.2.1	Individual Tile Design & Drawings (Test,ETU, & Flight)
134	4.1.6.3.2.1	Tile Test Unit Fabrication & Inspection (20 Tiles)
135	4.1.6.3.2.1	Tile Test Unit Delivery (Different Tiles @ Diff Times)
136	4.1.6.3.2.1	Tile and Fiber Wrapping
137	4.1.6.3.2.1	Design
138	4.1.6.3.2.1	Tile and Fiber Wrap Installation Procedure
139	4.1.6.3.2.1	Material Evaluation and Selection
140	4.1.6.3.2.1	Material Testing

Anti-Coincidence Detector Subsystem Schedule (Preliminary)

ID	WBS	Task Name
141	4.1.6.3.2.1	Adhesive Testing
142	4.1.6.3.2.1	Opaque Testing
143	4.1.6.3.2.1	Outgassing Testing
144	4.1.6.3.2.1	Material Selection Review
145	4.1.6.3.2.2	Wave Shifting Fibers Procurement
146	4.1.6.3.2.2	Fiber/Fiber Thermal Coupling (Ferilabs Design & Fab)
147	4.1.6.3.2.2	Assembly & Test (4.5 days to cut,polish,splice, & test)
148	4.1.6.3.2.2	Procure Tile & Fiber Wrapping Materials
152	4.1.6.3.2.3	Photo-Multiplier Tube (PMT) and Divider Assembly Procurement
153	4.1.6.3.2.3	Procurement Process
154	4.1.6.3.2.3	Prepare Procurement Package
155	4.1.6.3.2.3	Issue Procurement Synopsis for PMTs
156	4.1.6.3.2.3	Issue RFP for PMTs
157	4.1.6.3.2.3	Vendors Respond to RFP for PMTs
158	4.1.6.3.2.3	Vendor Selection and PMT Contract Award
159	4.1.6.3.2.3	Vendor Fabricate/Test PMTs
160	4.1.6.3.2.3	Receive ETU PMTs
161	4.1.6.3.2.3	Receive Flight PMTs
162	4.1.6.3.2.5	Tile Detector Assy (TDA) Proc/Fab/Assy/Test for DAQ I/F, Full Scale EM (Qual), & Calibration Units
163	4.1.6.3.2.5	ETU Tile Fabrication & Inspection (20 Tiles)
164	4.1.6.3.2.5	ETU Tile Delivery
165	4.1.6.3.2.5	ETU TDA Assembly (20 TDA's)
166	4.1.6.3.2.5	ETU TDA Acceptance Testing by 660

Anti-Coincidence Detector Subsystem Schedule (Preliminary)

ID	WBS	Task Name	2000				2001				2002				2003				2004				2005				2006				2007		
			4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3			
167	4.1.6.3.2.5	Clear Fiber Extension Cable - PMT/Fiber Connector - ETU									12/3		2/6																				
169	4.1.6.3.2.5	Clear Fiber Extension Cable - WSF/Clear Fiber Connector - ETU									12/3		2/6																				
171	4.1.6.3.2.7	Tile Detector Assy (TDA) Proc/Fab/Assy/Test for Flight Unit										3/8			1/7																		
180	4.1.6.3.3	Shell Subassembly						12/1							2/25																		
181	4.1.6.3.3.1	Shell Subassembly Analysis / Design						12/1						4/3																			
182	4.1.6.3.3.1	Requirements Definition						1/16					6/22																				
183	4.1.6.3.3.1	Design						12/1						1/18																			
184	4.1.6.3.3.1	Analysis						12/1						12/17																			
185	4.1.6.3.3.1	Material Studies						2/5					10/19																				
186	4.1.6.3.3.1	Procurement Specification and Award										1/22		4/3																			
187	4.1.6.3.3.3	Shell Subassembly Proc/Fab/Assy/Test for Full Scale EM (Qual) Unit										4/4		9/17																			
191	4.1.6.3.3.4	Shell Subassembly Proc/Fab/Assy/Test for DAQ I/F and Calibration Units											9/3		11/4																		
194	4.1.6.3.3.5	Shell Subassembly Proc/Fab/Assy/Test for Flight Unit												11/1		2/25																	
198	4.1.6.3.4	Tile Mounting Hardware & Assemblies									10/1				3/7																		
199	4.1.6.3.4.1	TDA Tiedown Prototype									10/1			6/6																			
200	4.1.6.3.4.1	Design									10/1			3/27																			
201	4.1.6.3.4.1	Analysis									11/6			3/26																			
202	4.1.6.3.4.1	Fabrication & Assembly										3/28		5/22																			
203	4.1.6.3.4.1	Testing											5/23		6/6																		
204	4.1.6.3.4.1	Static Load/Thermal											5/23		5/30																		
205	4.1.6.3.4.1	Vibration											5/31		6/6																		
206	4.1.6.3.4.3	Tile Mounting Hardware & Assembly Proc/Fab/Assy/Test for Full Scale EM (Qual) Unit											7/1		10/2																		
207	4.1.6.3.4.3	TDA Tiedown - ETU											7/1		10/2																		

Anti-Coincidence Detector Subsystem Schedule (Preliminary)

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Anti-Coincidence Detector Subsystem Schedule (Preliminary)

ID	WBS	Task Name		2000				2001				2002				2003				2004				2005				2006				2007		
			4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3				
247	4.1.6.4.3	Flight Unit A Test												6/21		9/23																		
248	4.1.6.4.4	Analog ASIC Design / Procurement							9/12								9/30																	
249	4.1.6.4.4	1st Analog Test ASIC							9/12						3/19																			
260	4.1.6.4.4	2nd Analog Test ASIC							2/6								9/30																	
261	4.1.6.4.4	Addition of features							2/6						3/5																			
262	4.1.6.4.4	Block diagram							3/6						3/30																			
263	4.1.6.4.4	Pre Layout simulations							4/2						4/6																			
264	4.1.6.4.4	Design Layout							4/9						5/18																			
265	4.1.6.4.4	Change to Agilent Process													5/21																			
266	4.1.6.4.4	Submit Designs to Foundry (Monthly)							5/21								6/26																	
267	4.1.6.4.4	Receive ASIC's (3 Month After Submittal to Foundry												8/23			9/30																	
268	4.1.6.4.4	Receive Flight ASICs																9/30																
269	4.1.6.4.6	Front-End Electronics and Event (FREE) Circuit Card Assembly (CCA)							3/9								5/21																	
270	4.1.6.4.6.1	FREE CCA Analysis / Design							3/9								3/14																	
271	4.1.6.4.6.1	Subsystem Definition of FREE Boards							3/9						7/30																			
278	4.1.6.4.6.1	Design Process for FREE Boards												7/31			10/17																	
313	4.1.6.4.6.1	Layout FREE PCB													4/26		3/14																	
335	4.1.6.4.6.3	FREE CCA - Proc/Fab/Assy/Test for DAQ I/F Unit and Full Scale EM													4/5		3/28																	
351	4.1.6.4.6.5	FREE CCA - Proc/Fab/Assy/Test for Flight Unit & Spares														9/30		5/21																
356	4.1.6.4.9	BEA Electronics Mounting Hardware and Assemblies							3/21								6/9																	
357	4.1.6.4.9.1	BEA Electronics Mounting Hardware & Assembly Analysis / Design							3/21								6/26																	
359	4.1.6.4.9.3	BEA Electronics Mounting Hardware & Assembly Proc/Fab/Assy/Test for Full Scale EM (Qual) Unit													7/5		9/27																	
362	4.1.6.4.9.4	BEA Electronics Mounting Hardware & Assembly Proc/Fab/Assy/Test for DAQ I/F and Calibration Units													8/2		10/28																	

Anti-Coincidence Detector Subsystem Schedule (Preliminary)

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Anti-Coincidence Detector Subsystem Schedule (Preliminary)

ID	WBS	Task Name
393	4.1.6.5.1.3	Shield Material Procurement
394	4.1.6.5.1.3	Blanket Material Procurement
395	4.1.6.5.1.3	Fabrication of Micrometeorite Shield/Thermal Blanket
397	4.1.6.5.1.4	Micrometeoroid Shield / Thermal Blanket Proc/Fab/Assy/Test for Calibration Units
399	4.1.6.5.1.5	Micrometeoroid Shield / Thermal Blanket Proc/Fab/Assy/Test for Flight Unit
401	4.1.6.5.2	Micrometeoroid Shield / Thermal Blanket Mounting and Assembly Hardware
402	4.1.6.5.2.1	Micrometeoroid Shield / Thermal Blanket Mounting and Assembly Hardware Analysis / Design
405	4.1.6.5.2.3	Micrometeoroid Shield / Thermal Blanket Mounting Hardware & Assembly Proc/Fab/Assy/Test for Full Scale EM (Qual) Unit
406	4.1.6.5.2.4	Micrometeoroid Shield / Thermal Blanket Mounting Hardware & Assembly Proc/Fab/Assy/Test for DAQ I/F and Calibration Units
407	4.1.6.5.2.5	Micrometeoroid Shield / Thermal Blanket Mounting Hardware & Assembly Proc/Fab/Assy/Test for Flight Unit
411	4.1.6.5.4	Micrometeoroid Shield / Thermal Blanket Full Scale EM (Qual) Unit Integration & Test
412	4.1.6.5.5	Micrometeoroid Shield / Thermal Blanket Calibration Unit Integration & Test
413	4.1.6.5.6	Micrometeoroid Shield / Thermal Blanket Flight Unit Integration & Test
414	4.1.6.6	Flight Software Development
415	4.1.6.6.1	ACD Flight Software Design
416	4.1.6.6.1	Requirements Specification/Definition
423	4.1.6.6.1	Requirements Specification of ACD Electronics Simulator
426	4.1.6.6.1	Requirements Specification to Spacecraft Interface
430	4.1.6.6.1	Requirements Specification to Spacecraft Simulator
433	4.1.6.6.1	Software Architecture Design
434	4.1.6.6.1	ACD Electronics Interface Code Design
435	4.1.6.6.1	ACD Electronics Simulator Design
436	4.1.6.6.1	Spacecraft Interface Code Design

Anti-Coincidence Detector Subsystem Schedule (Preliminary)

ID	WBS	Task Name	2000				2001				2002				2003				2004				2005				2006				2007		
			4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3			
437	4.1.6.6.1	Spacecraft Simulator Code Design									4/15		6/1																				
438	4.1.6.6.1	ACD Engineering Software Architecture Design Document									6/3		7/15																				
439	4.1.6.6.1	Delivery of ACD Engineering Software Architecture Document									7/16		7/17																				
440	4.1.6.6.1	Full Scale EM (Qual) & Calibration Unit Design											2/3		2/28																		
443	4.1.6.6.1	Flight Code Design													11/3		2/4																
447	4.1.6.6.2	ACD TEM Operating System									12/11																12/30						
453	4.1.6.6.3	ACD Application Software Code & Unit Test											10/9				4/15																
454	4.1.6.6.3	DAQ I/F Test Unit											10/9			2/28																	
455	4.1.6.6.3	ACD Software Coding											10/9			2/28																	
456	4.1.6.6.3	Receive ACD Electronics prototype board											12/2		12/2																		
457	4.1.6.6.3	Receive ACD TEM board from Stanford											12/2		12/2																		
458	4.1.6.6.3	ACD DAQ I/F Test Unit Testing											12/3		2/4																		
459	4.1.6.6.3	Full Scale EM and Calibration (Cal) Unit													5/30		7/15																
460	4.1.6.6.3	Implement Code Changes													5/30		7/15																
461	4.1.6.6.3	Regression Testing w/ Simulator and EM Electronics													6/30		7/15																
462	4.1.6.6.3	Flight Code															2/3		4/15														
463	4.1.6.6.3	Coding of Flight Software															2/3		3/15														
464	4.1.6.6.3	Regression Testing of S/W w/ Simulators and Available Electronics															3/17		4/15														
465	4.1.6.6.4	ACD Support Software									12/11																12/30						
466	4.1.6.6.4.1	Bench Test Equipment Software Development									12/11																12/30						
473	4.1.6.6.4.2	Configuration Management S/W									12/11																12/30						
476	4.1.6.6.6	DAQ I/F Test Unit Software Integration & Test											10/15			4/30																	
477	4.1.6.6.6	ACD Functional Testing with Simulators											10/15			12/13																	

Anti-Coincidence Detector Subsystem Schedule (Preliminary)

ID	WBS	Task Name
478	4.1.6.6.6	ACD M Functional Testing with EM electronics boards
479	4.1.6.6.6	ACD S/W Documentation
480	4.1.6.6.6	Delivery of DAQ I/F Test Documentation
481	4.1.6.6.7	Full Scale EM & Calibration Unit Software Integration & Test
482	4.1.6.6.7	ACD Full Scale EM & Cal Unit Calibration
483	4.1.6.6.7	Integration of ACD Electronics Simulator into SC Telemetry Simulator
484	4.1.6.6.7	Performance Testing of Code
485	4.1.6.6.7	ACD S/W Documentation
486	4.1.6.6.7	Delivery of ACD S/W Documentation
487	4.1.6.6.8	Flight Unit Software Integration & Test
488	4.1.6.6.8	Calibration of ACD Flight Code
489	4.1.6.6.8	Testing with Electronics/SC Simulators at SLAC
490	4.1.6.6.8	ACD Flight Hardware Initial Software Delivered to SLAC
491	4.1.6.6.8	Integration Testing with Spacecraft S/W at SLAC
492	4.1.6.6.8	Generate Test Results
493	4.1.6.6.8	Integration Testing at SC Assembly Location
494	4.1.6.6.8	Prep Final Flight Software for Delivery
495	4.1.6.6.8	Delivery of Final Flight Software
496	4.1.6.6.8	Post Delivery Maintenance/Repair/Changes
497	4.1.6.6.9	Flight Software Independent Verification & Validation (IV&V) Support
498	4.1.6.7	Hardware / Software Integration & Test
499	4.1.6.7.1	Miscellaneous LAT Interface / Assembly Hardware
500	4.1.6.7.1.1	Miscellaneous LAT Interface / Assembly Hardware Analysis / Design

Anti-Coincidence Detector Subsystem Schedule (Preliminary)

ID	WBS	Task Name	2000				2001				2002				2003				2004				2005				2006				2007		
			4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3			
501	4.1.6.7.1.1	Develop ACD Mechanical Verification Test Plan						4/2					4/27																				
502	4.1.6.7.1.1	Develop ACD Mechanical Integration Procedure												12/2		1/6																	
503	4.1.6.7.1.1	Develop ACD Handling Procedure												7/16		7/31																	
504	4.1.6.7.1.3	Miscellaneous LAT Interface / Assembly Hardware Proc/Fab/Assy/Test for Full Scale EM (Qual) Unit												6/26		12/4																	
507	4.1.6.7.1.4	Miscellaneous LAT Interface / Assembly Hardware Proc/Fab/Assy/Test for DAQ I/F Test and Calibration Units												7/30		1/7																	
509	4.1.6.7.1.5	Miscellaneous LAT Interface / Assembly Hardware Proc/Fab/Assy/Test for Flight Unit												1/8		6/13																	
511	4.1.6.7.3	Full Scale EM (Qual) Unit Hardware / Software Integration & Test												1/3		10/2																	
512	4.1.6.7.3	ETU-ACD Mechanical Integration												1/3		2/17																	
513	4.1.6.7.3	ETU Mechanical Verification Test Program												2/18		3/13																	
514	4.1.6.7.3	Replace Active Tiles from EM w/Mass Models & Replace Electronics w/Dummy Heat Loads												3/14		10/2																	
515	4.1.6.7.4	DAQ I/F Test and Calibration Unit Hardware / Software Integration & Test												10/15		6/10																	
516	4.1.6.7.4	DAQ I/F Test Unit Assembly & Test												10/15		11/19																	
517	4.1.6.7.4	Calibration Unit												4/10		6/10																	
529	4.1.6.7.5	Flight Unit Hardware / Software Integration & Test												4/29		1/14																	
530	4.1.6.7.5	Flight ACD Mechanical Integration												4/29		6/30																	
531	4.1.6.7.5	Flight Verification Test Program													7/1		1/14																
532	4.1.6.8	LAT Instrument Integration & Test Support																															
533	4.1.6.8.1	BFEM ACD Integration & Test Support (Balloon Flight)						6/15			8/3																						
534	4.1.6.8.1	ACD Suborbital Integration @ GSFC						6/15			7/13																						
535	4.1.6.8.1	Suborbital Flight Campaign						7/16			8/3																						
536	4.1.6.8.3	DAQ I/F Test, Full Scale EM, and Calibration Unit ACD Integration & Test Support @ SLAC												2/28			5/25																
539	4.1.6.8.4	Flight Unit Integration & Test Support (Flight LAT I&T)													4/27		2/28																
541	4.1.6.9	Mission Integration & Test Support															3/31																

Anti-Coincidence Detector Subsystem Schedule (Preliminary)

ID	WBS	Task Name
542	4.1.6.9.1	Provide ACD Subsystem Support during Flight LAT I&T w/Spacecraft
545	4.1.6.9.2	Provide ACD Subsystem Support during Launch Site / Launch Operations
547	4.1.6.A	Mission Operations & Data Analysis (MO&DA)
558	4.1.6.B	Ground Support Facilities and Equipment
559	4.1.6.B.1	Ground Support Facilities / Facility Modifications
560	4.1.6.B.2	Mechanical Ground Support Equipment
561	4.1.6.B.2.1	ACD Turnover / Assembly Dolly
562	4.1.6.B.2.1	Requirements Definition
563	4.1.6.B.2.1	Design and Analysis
564	4.1.6.B.2.1	Fabrication
565	4.1.6.B.2.1	Delivery
566	4.1.6.B.2.1	Functional and Proof Testing
567	4.1.6.B.2.2	Shell Handling Assembly
568	4.1.6.B.2.2	Requirements Definition
569	4.1.6.B.2.2	Design and Analysis
570	4.1.6.B.2.2	Fabrication
571	4.1.6.B.2.2	Delivery
572	4.1.6.B.2.2	Functional and Proof Testing
573	4.1.6.B.2.3	TDA Handling Cases
574	4.1.6.B.2.3	Design Specification
575	4.1.6.B.2.3	Procurement Specification
576	4.1.6.B.2.3	Delivery
577	4.1.6.B.2.4	Base Frame Handling Assembly

Anti-Coincidence Detector Subsystem Schedule (Preliminary)

ID	WBS	Task Name
578	4.1.6.B.2.4	Requirements Definition
579	4.1.6.B.2.4	Design and Analysis
580	4.1.6.B.2.4	Fabrication
581	4.1.6.B.2.4	ETU & Flight Delivery
582	4.1.6.B.2.4	Functional and Proof Testing
583	4.1.6.B.2.4	ETU & Flight Base Frame Handling Dolly Available
584	4.1.6.B.2.5	Grid Interface Template
585	4.1.6.B.2.5	Receive from SLAC
586	4.1.6.B.2.5	Return to SLAC
587	4.1.6.B.2.6	ACD Shipping Container w/Nitrogen Purge
588	4.1.6.B.2.6	Transportation Specification Document
589	4.1.6.B.2.6	Shipping Container Selection
590	4.1.6.B.2.6	Shipping Container Interface Design and Analysis
591	4.1.6.B.2.6	Interface Fabrication
592	4.1.6.B.2.6	Interface Delivery
593	4.1.6.B.2.6	Shipping Container Integration
594	4.1.6.B.2.6	Shipping Container Testing
595	4.1.6.B.2.7	ACD Lifting Slings
596	4.1.6.B.2.7	Requirements Definition
597	4.1.6.B.2.7	Design and Analysis
598	4.1.6.B.2.7	Fabrication
599	4.1.6.B.2.7	Delivery of ETU & Flight Lifting Slings
600	4.1.6.B.2.7	Initial Proof Testing and NDE

Anti-Coincidence Detector Subsystem Schedule (Preliminary)

ID	WBS	Task Name	2000				2001				2002				2003				2004				2005				2006				2007			
			4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4			
601	4.1.6.B.2.7	Annual Proof Testing and NDE													8/11	8/15																		
602	4.1.6.B.2.7	ETU & Flight GSE Lifting Slings													8/18	8/18																		
603	4.1.6.B.3	Electrical Ground Support Equipment											5/3		9/24																			
604	4.1.6.B.3.4	FREE Board-Level GSE											5/3		9/24																			